Workshop Schedule

Day (1) - Friday, 23 September 2016

8:30 A.M. - 9:00 A.M. Entry from the main doors Zewail City (Main Entrance)

9:00 A.M. - 9:10 A.M. Welcome Speech (Room 127)

9:10 A.M. - 10:30 A.M. Navigating LabVIEW, Problem Solving, and Implementing a VI. (Lecture) (Room 127)

10:30 A.M. - 11:40 A.M. Programming Types and Acquiring and Handling Data. (Lecture) (Room 127)

11:40 P.M. - 1:00 P.M. Break (Main Entrance)

1:00 P.M. - 5:00 P.M. LabVIEW Examples Workshop (Room 127)

Day (2) - Saturday, 24 September 2016

9:00 A.M. - 9:30 A.M. Entry from the main doors of Zewail City (Main Entrance)

9:30 A.M. - 11:00 A.M. Instrument Control, and Creating and Distributing Applications. (Lecture) (Room 127)

11:00 A.M. - 12:30 A.M. Simulation of instruments and Complex Systems. (Lecture) (Room 127)

12:30 P.M. - 1:00 P.M. Break (Main Entrance)

1:00 P.M. - 5:00 P.M. UNIDAQ Prototype FPGA Workshop (Room 127)