

## Workshop Schedule

### Day (1) - Friday, 23 September 2016

8:30 A.M. - 9:00 A.M.	Entry from the main doors Zewail City (Main Entrance)
9:00 A.M. - 9:10 A.M.	<b>Welcome Speech</b> (Room 127)
<b>9:10 A.M. - 10:30 A.M.</b>	<b>Navigating LabVIEW, Problem Solving, and Implementing a VI. (Lecture)</b> (Room 127)
<b>10:30 A.M. - 11:40 A.M.</b>	<b>Programming Types and Acquiring and Handling Data. (Lecture)</b> (Room 127)
11:40 P.M - 1:00 P.M.	Break (Main Entrance)
<b>1:00 P.M. - 5:00 P.M.</b>	<b>LabVIEW Examples Workshop</b> (Room 127)

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### Day (2) - Saturday, 24 September 2016

9:00 A.M. - 9:30 A.M.	Entry from the main doors of Zewail City (Main Entrance)
<b>9:30 A.M. - 11:00 A.M.</b>	<b>Instrument Control, and Creating and Distributing Applications. (Lecture)</b> (Room 127)
<b>11:00 A.M. - 12:30 A.M.</b>	<b>Simulation of instruments and Complex Systems. (Lecture)</b> (Room 127)
12:30 P.M. - 1:00 P.M.	Break (Main Entrance)
<b>1:00 P.M. - 5:00 P.M.</b>	<b>UNIDAQ Prototype FPGA Workshop</b> (Room 127)